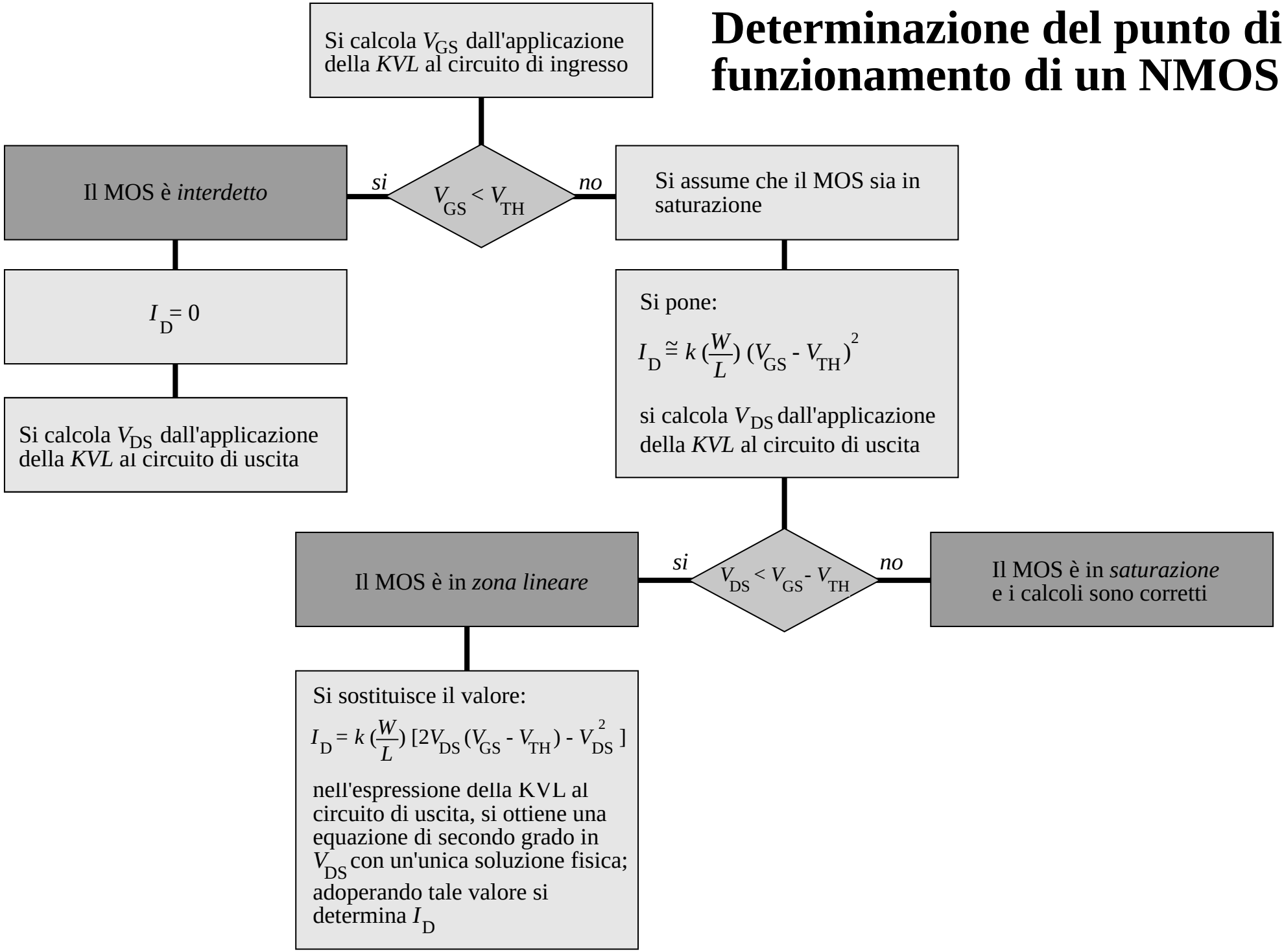Determinazione del punto di funzionamento di un NMOS